FIG. 1A
RELATED ART

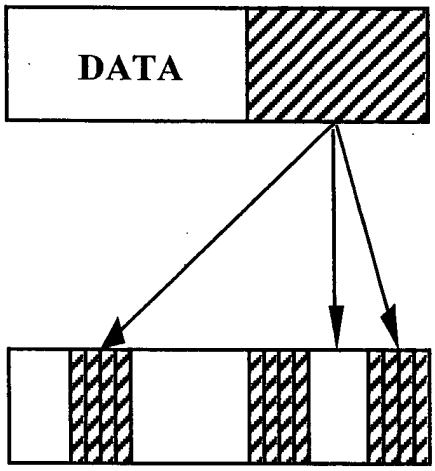
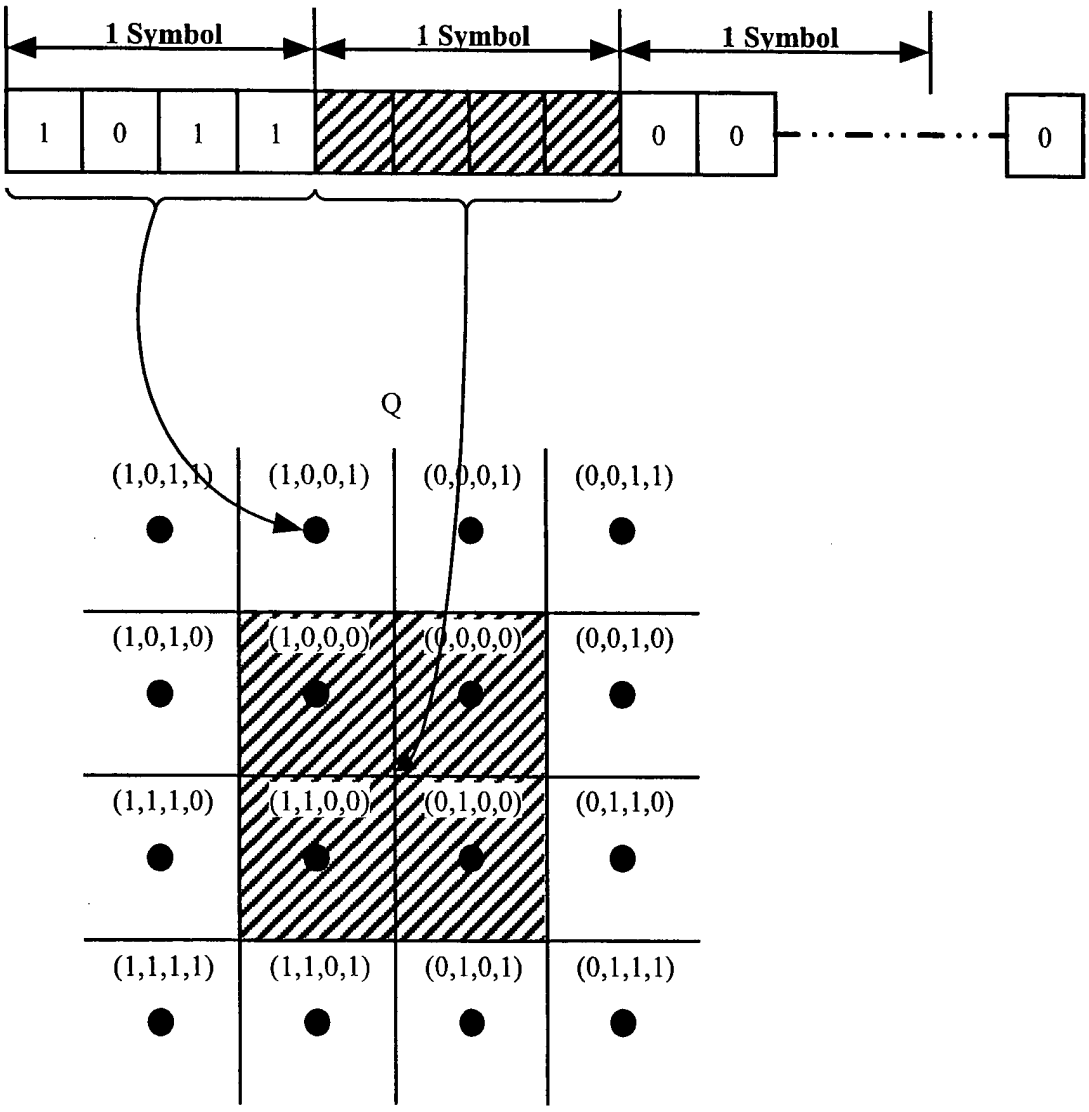


FIG. 1B
RELATED ART



DTX Bit Mapping

FIG. 2A
RELATED ART

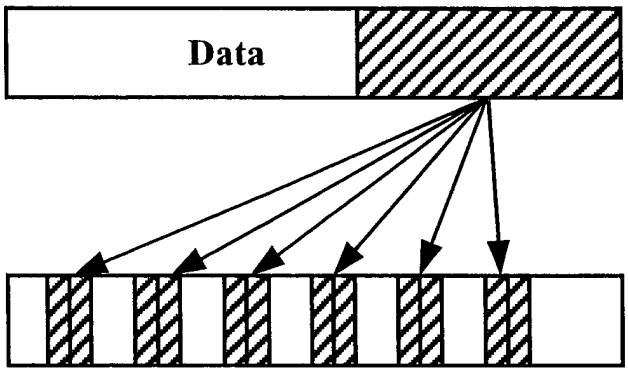
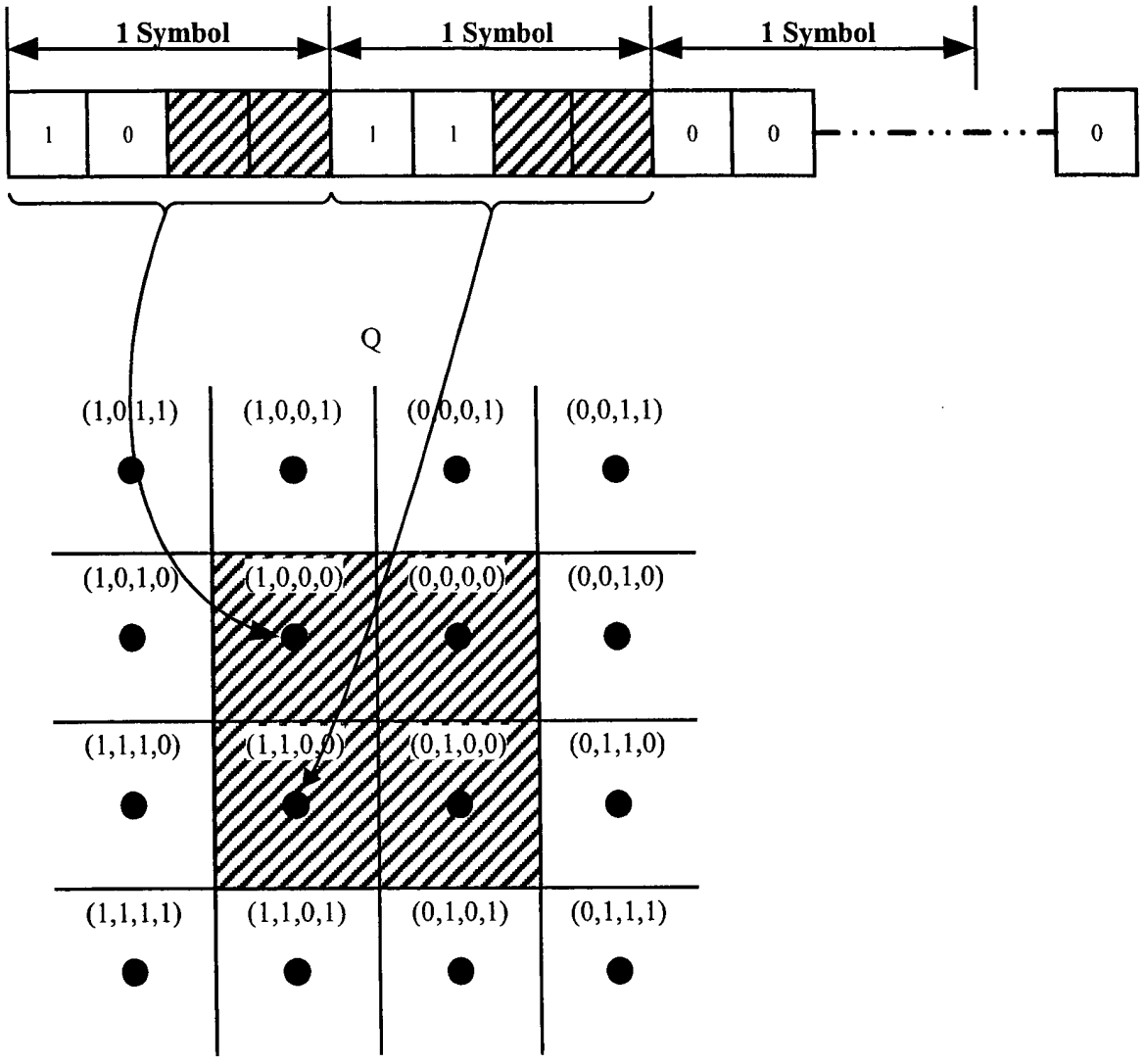


FIG. 2B
RELATED ART



DTX Bit Mapping

FIG. 3

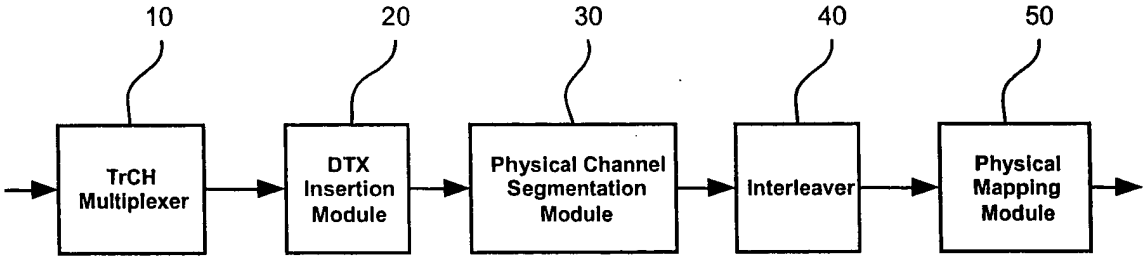
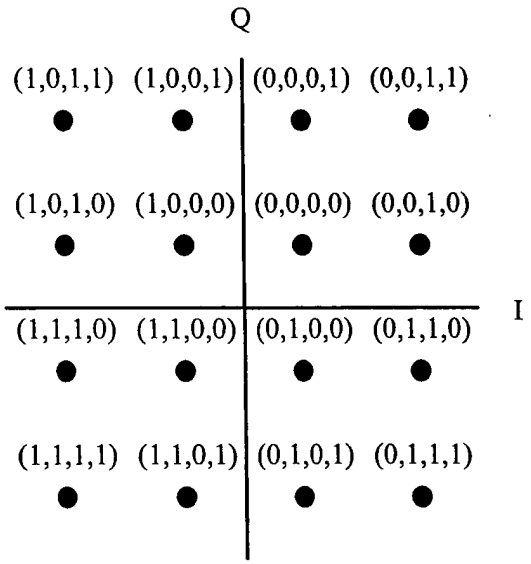
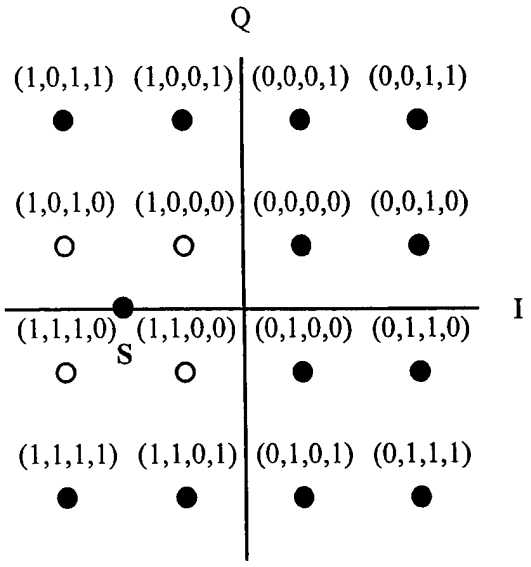

10
20
30
40
50
TrCH Multiplexer
DTX Insertion Module
Physical Channel Segmentation Module
Interleaver
Physical Mapping Module

FIG. 4A



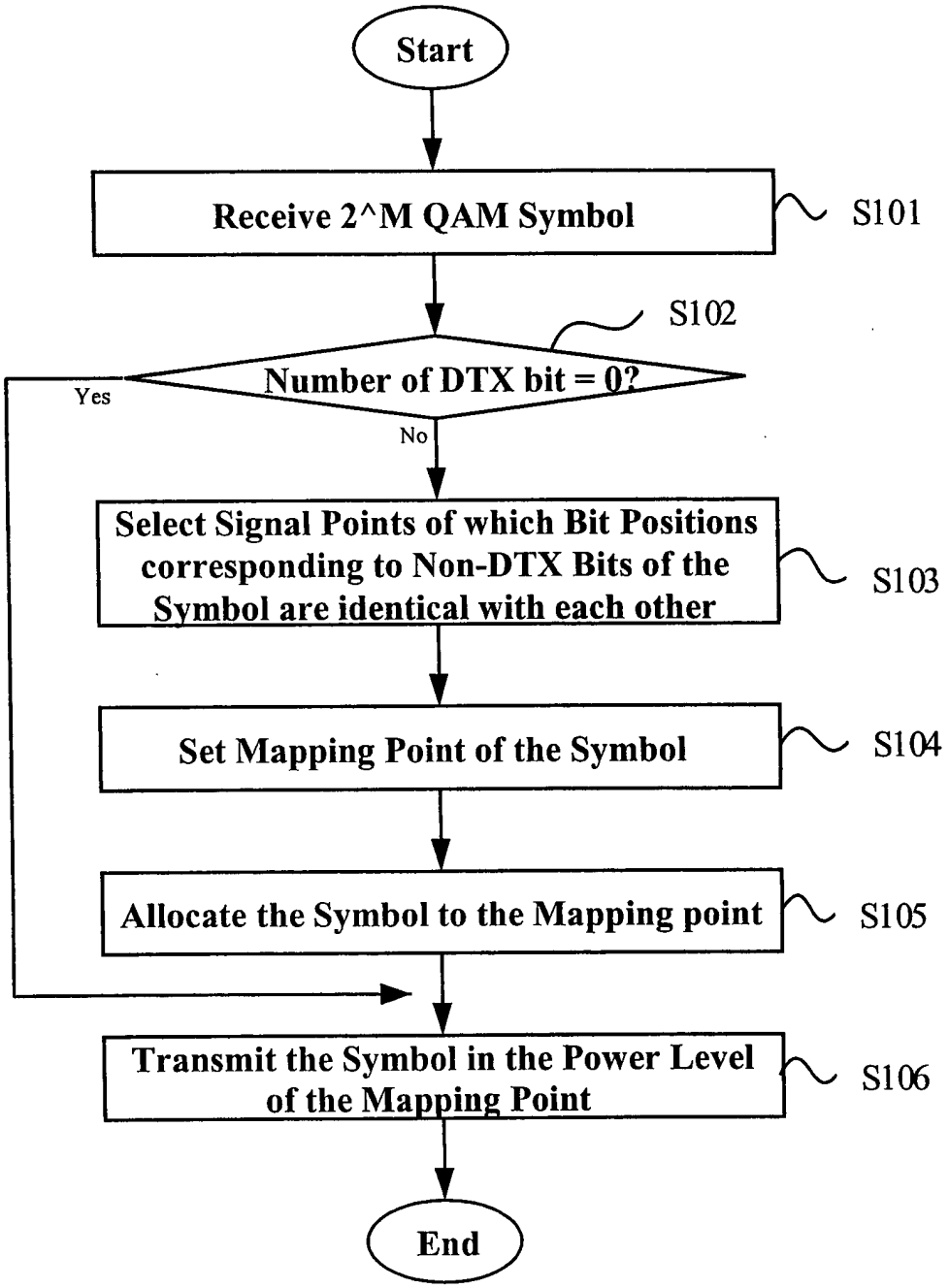
16 QAM Constellation

FIG. 4B



1XX0 Mapping Point (X is DTX bit)

FIG.5


Start
Receive 2^M QAM Symbol
S101
S102
Number of DTX bit = 0?
Yes
No
Select Signal Points of which Bit Positions corresponding to Non-DTX Bits of the Symbol are identical with each other
S103
Set Mapping Point of the Symbol
S104
Allocate the Symbol to the Mapping point
S105
Transmit the Symbol in the Power Level of the Mapping Point
S106
End